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System Requirements:. Recent Drivers. Zoom - M810. For more

information, contact software publisher. Windows:8. 3 version support. The present invention relates to a test arrangement for a power transistor. It is known that, in the case of a power transistor with a voltage-dependent resistance, at least one of the parameters of the switching characteristics, i.e., the voltage or the current at which the transfer into the conducting state begins and the current or the voltage at which the transfer into the conducting state ceases, can be varied between two different values depending on the prevailing voltage.

In the case of a FET (field-effect transistor) in which the gate-source voltage is utilized for controlling the conductivity of the FET, there is usually no direct control of the transfer into the conducting state; in the case of a MOS-FET (insulated-gate field-effect transistor) also no direct control of the transfer into the conducting state occurs. The transfer into the conducting state is possible only in that way that a transfer into the conducting state occurs only in dependence on the voltage at the gate or at the source. In the case of a power MOS-FET, there is no direct

control of the transfer into the conducting state, but such a controllability can be established indirectly by means of a so-called body effect. In this case, a fixed potential is established in the substrate of the power MOS-FET by means of a body contact, and this fixed potential is utilized for varying the threshold voltage. The body contact is here usually realized in an N-type substrate by means of a doping of the P-type semiconductor substrate of the power MOS-FET. In such a power MOS-FET with a body effect, an impedance is usually

established in the substrate. This impedance cannot be directly utilized for purposes of testing the power MOS-FET because a change of the impedance is associated with a change in the potential of the body contact and, therefore, with a change in the threshold voltage of the power MOS-F

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